



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

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Applicant: Yoshihiro Yoneda

Serial No. 09/820,470

Filed: March 29, 2001

For: SURFACE-MOUNTING SUBSTRATE
STRUCTURE COMPRISING
SUBSTRATE AND PART MOUNTED
ON THE SUBSTRATE

Examiner: Ishwarbhai B. Patel

Art Unit: 2827

Attny Docket: 082-01

CERTIFICATE OF MAILING

I hereby certify that this
correspondence is being transmitted
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postage, and addressed to
Commissioner for Patents,
Washington, DC 20231,
on April 16, 2003

Kim Gubitosi
Kim Gubitosi

Dated: April 16, 2003

COVER LETTER

Commissioner for Patents
Washington, DC 20231

Sir:

Submitted are the following documents:

- 1) This cover letter;
- 2) Supplemental IDS Statement w/Form 1449,
and References (17 pages); and
- 3) Post card return receipt.

No additional fees are believed to be required. In the event that an additional fee is required with respect to this communication, the Commissioner is hereby authorized to charge any additional fees, or credit any overpayment, to Paul & Paul Deposit Account No. 16-0750.

Date: April 16, 2003

Respectfully Submitted,

John J. Simkanich
John J. Simkanich
Regis. No. 26,036
Paul & Paul
2900 Two Thousand Market Street
Philadelphia, PA 19103
(215) 568-4900
(FAX) 215-657-5057

Order No. 1395



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial Number: 09/820,470
Inventor: Yoshihiro Yoneda
Filed: March 29, 2001
For: SURFACE-MOUNTING SUBSTRATE STRUCTURE COMPRISING
SUBSTRATE AND PART MOUNTED ON THE SUBSTRATE
Examiner: Ishwarbhai B. Patel
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT WITH FORM 1449

Commissioner for Patents
Washington, DC 20231

Sir:

Applicants request consideration of the following information disclosure statement, pursuant to 37 CFR 1.97(e)(1) and 1.97(c)(1). No final action has been mailed under Sec. 1.113, no notice of allowance has been issued under Sec. 1.311. No information contained in the IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge, information and belief of the undersigned, upon reasonable inquiry, no item in the IDS was known to any individual designated in §1.56(c) more than 3 months prior to the date of this IDS.

Submitted herewith is a PTO Form 1449. Also submitted are copies of:

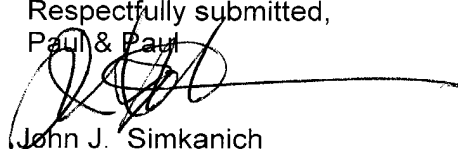
1. JP9223715, (1997, Aug 26), Watanabe Masaki, for CONNECTING STRUCTURE OF FLIP CHIP OR FLIP CHIP CARRIER;
2. JP10013003, (1998, Jan 16), Kishigami Masamitsu, for SEMICONDUCTOR DEVICE;

Remarks on Relevancy

All documents above-listed are submitted in Japanese with an English abstract. The requirements specified by MPEP 609A(3) and 37 CFR 1.98 are considered to have been satisfied.

Respectfully submitted,
Paul & Paul

Date: April 16, 2003

by: 
John J. Simkanich
Regis. No. 26,036
2900 Two Thousand Market Street
Philadelphia, PA 19103
(215) 568-4900
FAX 215-567-5057

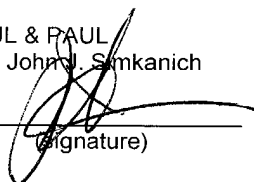
CERTIFICATE OF MAILING

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(date)

Commissioner for Patents
Washington, D.C. 20231

PAUL & PAUL
by: John J. Simkanich


(signature)

INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)

APR 21 2003

Docket Number (Optional) 082-01	Application Number 09/820,470
Applicant(s) Yoshihiro Yoneda	
Filing Date March 29, 2001	Group Art Unit 3729

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		JP9223715	26 Aug 97	Japan				
		JP10013003	16 Jan 98	Japan				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.